

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10814968			
<b>Filing Date:</b>	31-Mar-2004			
<b>Title of Invention:</b>	Integrated circuit having a memory cell array capable of simultaneously performing a data read operation and a data write operation			
First Named Inventor/Applicant Name:	Kyo-Min Sohn			
<b>Filer:</b>	Frank Chau/Richard D. Ratchford, Jr.			
<b>Attorney Docket Number:</b>	8021-225 (SS-18860-US)			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with \$0 paid	1251	1	120	120

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>120</b>